

Faculty Name: T. VIJAYA KUMAR

Faculty Photo:



Faculty Description:

Mr. T. Vijay Kumar is Associate Professor in Department of Electronics and Communication Engineering at Dr. K. V . Subba Reddy Institute of Technology, Kurnool.

Profile Tab:

Qualifications

Qualification	Institution / University	Year
Bachelor's degree	Department of Electronics and Communication Engineering, from Vijay Rural Engineering College, Nizamabad, JNTU, Hyderabad.	2003
Master's degree	Department of Electronics and Communications Engineering, JNTUCEA, JNTUA.	2011
Ph.D	Pursuing Ph. D from Vels University, Chennai.	-

Experience

Designation	Institution	From	To
Assistant Professor	Sri Kottam Tulasi Reddy Memorial College of Engineering, Kondair, Mahaboob Nagar Dist. A.P.	2005	2014
Associate Professor & HoD ECE	Dr. K. V. Subba Reddy Institute of Technology, Dupadu, Kurnool	2014	2019
Associate Professor	Dr.K.V.S.R.I. T, Dupadu, Kurnool	2019	Till Date

Awards / Achievement

1. Life Member of the Indian Society for Technical Education
2. Member in Institute of Research Engineers and Doctors

Responsibilities Tab:

Responsibilities

- Exam Section In charge in S.K.T.R.M.C.E., from 2009 - 2013
- In Charge HoD in SKTRMCE, from 2012-2013.
- Head Of Department of ECE in Dr.K.V.S.R.I.T. from 2014-2019.

Teaching Tab:

Expertise / list of subjects handled

Under Graduate Courses:

- Electronic Devices and Circuits
- Digital Signal Processing
- Electronic Measurements & Instrumentation
- Signals & Systems
- Network Analysis
- Linear Integrated Circuits & Analysis
- Wireless Communication
- Switching Theory and Logic Design
- Control Systems Engineering
- Embedded systems

Post Graduate Courses:

- Digital System Design

Research Tab:

Research Interest

- Wireless Communication Networks
- Digital Electronics

Publications tab:

Books

--

Conferences

1. “Survey on Secure Cognitive Radio Network with Optimal Resource Management “ in 5th International Conference on Innovative Recent Advancements in Engineering and Technology (ICRAET-2023) held on 19th & 20th May 2023 at Bengaluru, India .
2. Published paper on “Motion Estimation in video coding system by EDCA using Residue and quotient code“ in National Conference on Innovative Research in Engineering and Technology (NCIRET 2K15) held on 25th April 2015 at Dr. K.V. Subba Reddy Institute of Technology, Kurnool.
3. Waves’2008: A National Conference, Feb 5th , 2008, Organized by ECE Dept. Sri Kottam Tulasi Reddy Memorial College of Engineering, Kondair, Mahabub Nagar Dist, A.P

Journals

1. T. Vijay Kumar, “Implementation of lossless image compression using fuzzy based modified golomb rice encoding ” Journal Of Engineering Sciences Vol 14 Issue 02,2023.
2. T. Vijay Kumar, “VLSI Implementation Of Hybrid Median Filter Using Multi-Level Data Comparators” Journal Of Engineering Sciences Vol 14 Issue 02,2023

3. T. Vijay Kumar, "Turbo Codec: VLSI Implementation Of Turbo Encoder And Decoder Using Verilog" Journal Of Engineering Sciences Vol 14, Issue 02,2023
4. T. Vijay Kumar, "Implementation of Median data sorting using Multiplexer Logic." Journal Of Engineering Sciences ISSN 0377-9254,Vol 13, Issue 07, July 2022.
5. T. Vijay Kumar, "Implementation of Multibit Error detection and Correction using QAEC course for real time Communication Application." Journal of Resource Management & Technology ISSN 0745-6999,Volume 12, Issue 4, 2021
6. T. Vijay Kumar, "Salient Object Detection With SpatiOTemporal Background Priors For Video" Journal of engineering Sciences Vol 10, Issue 8, Aug / 2019 .
7. T. Vijay Kumar, "Effective and fast single image super resolution (SR) algorithm by combining clustering and collaborative representation (CCR)" International Journal of Engineering Research Vol.VII, Issue XII, DECEMBER 2018.
8. G Rajashekar and T. Vijay Kumar, "Fourier transform Based LMMSE analysis MIMO Channel Estimation" International Journal of Advanced Research in Electronics and Communication Engineering Vol. 5 Issue 5, May 2016, pp..1374-1388
9. K Pavithra and T. Vijay Kumar, "Implementation of Vehicular Monitoring and tracking based on ARM " International Journal of Innovative Research in Engineering Vol.II, Issue VII, DECEMBER 2015.
10. G Aswini and T. Vijay Kumar, "Watermark extraction from the spread Spectrum embedding Algorithm" International Journal of Professional Engineering Studies Vol. IV, Issue 4, DECEMBER 2014.

Workshops & FDPs

1. Participated in one week Faculty Development Programme on “Future Generation & Communication Networking Organized by Department of Electronics and Communication Engineering Jerusalem Engineering College ” held during 2nd January – 7th January, 2023.
2. Participated in five day online Faculty Development Programme on “Advanced Technologies & Challenges in Electronics and Communication Engineering” held during 8th – 12th March, 2022.
3. Participated in one week national level online faculty development program on “Signals, Systems and Transform Techniques” during 22nd - 27th November 2021 organized by the Department of Electrical & Electronics Engineering Maturi Venkata Subba Rao (MVSR) Engineering College (Autonomous), Hyderabad.
4. “A 3-Day Online FDP on CMOS Analog Integrated Circuits” organized by Department of ECE, SVR ENGINEERING COLLEGE, NANDYAL, ANDHRA PRADESH from 28.05.2020 to 30.05.2020.
5. Participated in the six day Faculty Development Program on Introspection & Gearing up for New Age Teaching Delivery - Post COVID 19’ conducted from 28th May 2020 to 02nd June 2020.
6. Organized DrishTI Online contest 2017 on 11.09.2018 by Texas Instruments India University Program at Dr K V S R I.T. Kurnool.
7. A 3-Days workshop in ‘Teachers Training Program on Instructional Techniques’ from July 27-29, 2008 conducted by NITTR Chennai at SKTRMCE, Kondair.
8. 5 –day Mission 10X Programme from Feb 28 – March 3. 2012 conducted by WIPRO at SKTRMCE, Kondair.
9. Organized one day workshop on Analog System Design using ASLKPro on 29th July 2015 at Dr K V S R I.T. Kurnool.

Blog Tab:
